

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant: Chambers

Art Unit: 2818

Serial No.: 10/696,539

Examiner: Le, Dung Anh

Filing Date: 10/29/2003

Docket No.: TI-36031

Customer No.: 23494

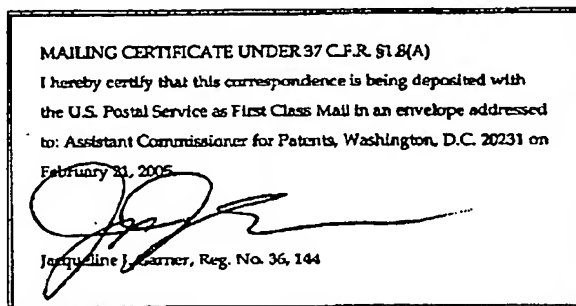
Conf. No.: 7816

Title: METHODS FOR FABRICATING A TRIPLE-GATE MOSFET
TRANSISTOREx Parte Quayle Amendment

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this paper.*

Assistant Commissioner of Patents
Washington, DC 20231
February 21, 2005

Dear Sir:



The following amendments and remarks are offered in response to the Examiner's Office Action dated 12/22/2004. They are respectfully submitted as a full and complete response to that Action.

Please amend the above-referenced application as follows:

The **Amendments to the Specification** begin on page 2.

The **Amendments to the Abstract** begin on page 3.

Amendments to the Claims are reflected in the listing of claims that begin on page 4 of this paper.

There are no **Amendments to the Drawings**.

The **Remarks** begin on page 7.